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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/767,439	01/30/2004	Nobuhisa Kumamoto	AI 318D1	7225
7590 09/22/2005			EXAMINER	
MR STEVEN M RABIN			CAO, PHAT X	
C/O RABIN & BERDO PC SUITE 500			ART UNIT	PAPER NUMBER
1101 14TH STREET NW			2814	
WASHINGTON, DC 20005			DATE MAILED: 09/22/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Office Astion Comments	10/767,439	KUMAMOTO ET AL.			
Office Action Summary	Examiner	Art Unit			
	Phat X. Cao	2814			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
Status					
 Responsive to communication(s) filed on <u>03 August 2005</u>. This action is FINAL. 2b) ☐ This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i>, 1935 C.D. 11, 453 O.G. 213. 					
Disposition of Claims					
4) ☐ Claim(s) 11-24 is/are pending in the application 4a) Of the above claim(s) 13-24 is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 11 and 12 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	n from consideration.				
Application Papers					
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the objected to by the Examiner 11) The oath or declaration is objected to by the Examiner 20 21 22 23 24 25 26 27 28 28 29 20 20 20 20 20 20 20 20 20 20 20 20 20	epted or b) objected to by the Idrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No. 09/504,874. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 1/04;4/05;5/05.	4) Interview Summary Paper No(s)/Mail Do 5) Notice of Informal F 6) Other:				

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DETAILED ACTION

1. Applicant's election without traverse of claims 11-12 in the reply filed on 8/3/05 is acknowledged.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Information Disclosure Statement

3. The information disclosure statement filed 1/30/04 fails to comply with the provisions of 37 CFR 1.97, 1.98 and MPEP § 609 because it is a form PTO-892 (not PTO-1449) cited from the parent application. It has no blank spaces for the examiner to initial, and the correct application number is not on each page of the list. It has been placed in the application file, but the information referred to therein has not been considered as to the merits. Applicant is advised that the date of any re-submission of any item of information contained in this information disclosure statement or the submission of any missing element(s) will be the date of submission for purposes of determining compliance with the requirements based on the time of filing the statement, including all certification requirements for statements under 37 CFR 1.97(e). See MPEP § 609.05(a).

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

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(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 11-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Corisis et al (US. 6,232,666).

Regarding claim 11, Corisis (Fig. 3D) discloses a production process for a semiconductor chip, comprising the steps of: providing an internal interconnection 46 on a semiconductor substrate 34; forming a surface protective film 64/62 over the internal interconnection 46; forming an opening 42 in the surface protective film 64/62 to expose a portion of the internal interconnection 46; forming a bump projecting 60 from the surface protective film on the portion of the internal interconnection 46 exposed through the opening 42; and forming a surface interconnection 36 having a smaller height than the bump 60 in a predetermined region on the surface protective film 64/62 except a portion thereof formed with the opening 42.

Regarding claim 12, Corisis's Fig. 3D further disclose that the bump 60 forming step includes the step of selectively depositing a conductive material 60 on the portion of the internal interconnection 46 exposed through the opening 42, and the surface

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interconnection 36 forming step includes the step of selectively depositing a conductive material in the predetermined region on the surface protective film 64/62 except the portion thereof formed with the opening 42.

6. Claims 11-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Hashimoto (US. 6,255,737).

Regarding claim 11, Hashimoto (Fig. 20A) discloses a production process for a semiconductor chip, comprising the steps of: providing an internal interconnection 334 on a semiconductor substrate 302; forming a surface protective film 340 over the internal interconnection 334; forming an opening in the surface protective film 340 to expose a portion of the internal interconnection 334; forming a bump 348 projecting from the surface protective film 340 on the portion of the internal interconnection 334 exposed through the opening; and forming a surface interconnection 346 having a smaller height than the bump 348 in a predetermined region on the surface protective film 340 except a portion thereof formed with the opening.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phat X. Cao whose telephone number is 571-272-1703. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PC

September 19, 2005

PHAT X. CAO PRIMARY EXAMINER